

# ビットエラー レートテスター

# BERT-C

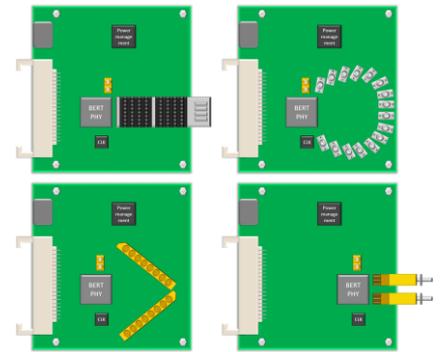


## BERT-HOST



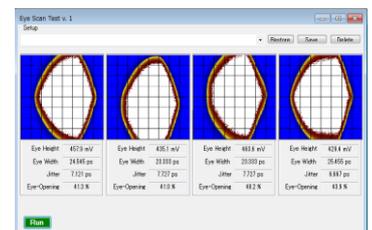
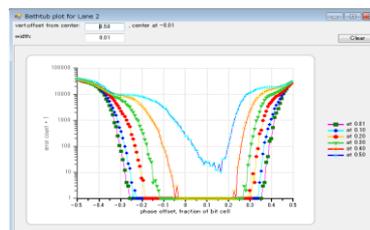
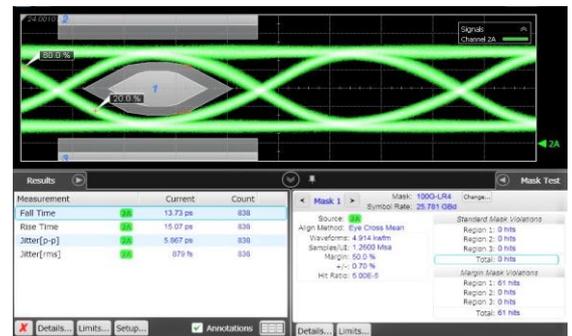
## BERT-POD

- プラガブルを直接挿入してエラーレート測定
- PODの交換で多種製品に対応可能
- LAN接続により複数台同時測定が可能



## BERT-POD-A (4 x 28G QSFP28 interface)

- 4 Lane Pattern Generator and Error Detector
- Data rate 22.0 – 30.0 Gb/s
- 1/8, 1/16 トリガクロック
- PRBS 2<sup>7</sup>-1, 2<sup>9</sup>-1, 2<sup>15</sup>-1, 2<sup>23</sup>-1, 2<sup>31</sup>-1
- 高速Eye Scan機能
- 消費電力モニタ

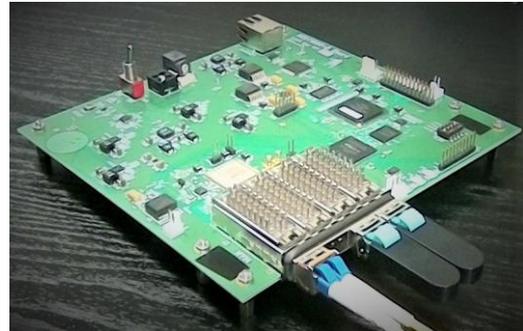


# ビットエラー レートテスター

# BERT-A BERT-B



**BERT100G-A**  
QSFP28プラグابل



**BERT100G-B**  
4 x SFP28プラグابل

Category	Parameter	
Pulse Pattern Generator	Bit Rate	22.0 – 30.0 Gbps
	Output Differential Voltage	750 mVpp
	Amplitude Adjustment	Range 100 – 0% / Step 25%
	PRBS Pattern	PN7, PN9, PN15, PN23, PN31
	Rise / Fall Time (20-80%)	Typ. 16ps
Error Detector	Jitter (RMS)	1.0 – 1.2 ps
	Bit Rate	22.0 – 30.0 Gbps
Reference Clock	Input Differential Voltage	110 – 1050 mVpp
	Reference Clock Stability	± 20 ppm
Monitor Clock	Monitor Clock Frequency	Rate / 8 or Rate / 16
	Monitor Clock Output Voltage	Typ. 400 mVpp
Power Supply	DC Input	+5 V / 4 A